

20 V dual P-channel Trench MOSFET 3 February 2016

Product data sheet

1. General description

Dual small-signal P-channel enhancement mode Field-Effect Transistor (FET) in a leadless medium power DFN2020-6 (SOT1118) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- 2 kV ElectroStatic Discharge (ESD) protection

3. Applications

- Relay driver
- High-speed line driver
- High-side load switch
- Switching circuits

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor		·					,
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-4.5	А
Static characteristics (per transistor)							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -2 A; T _j = 25 °C		-	58	67	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	6 5 4	D1 D2
2	G1	gate TR1		
3	D2	drain TR2		G1_ →_⊢┥╶┿╴╶┿╴┝┥└╴┯╶╌ <mark>┧</mark> G2
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1	Transparent top view DFN2020-6 (SOT1118)	S1 S2 017aaa260
7	D1	drain TR1	BI 112020 0 (0011110)	
8	D2	drain TR2		

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMDPB58UPE	DFN2020-6	DFN2020-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals; body 2 x 2 x 0.65 mm	SOT1118			

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMDPB58UPE	2A

8. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
Per transistor						
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-4.5	А
		V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-3.6	А
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-2.3	А

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Symbol	Parameter	Conditions		Min	Мах	Unit
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-14.4	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	515	mW
			[1]	-	1210	mW
		T _{sp} = 25 °C		-	8330	mW
Per device	•					
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	ain diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	-1.3	А
ESD maxin	num rating	,	1			
V _{ESD}	electrostatic discharge voltage	HBM; C = 100 pF; R = 1.5 kΩ	[3]	-	2000	V

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
 [2] Management & battering and battering and

[3] Measured between all pins.

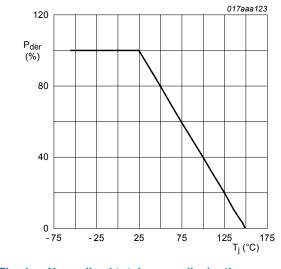


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

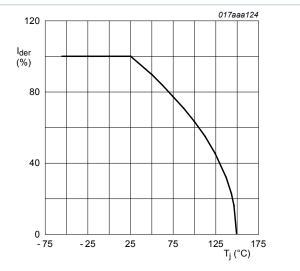
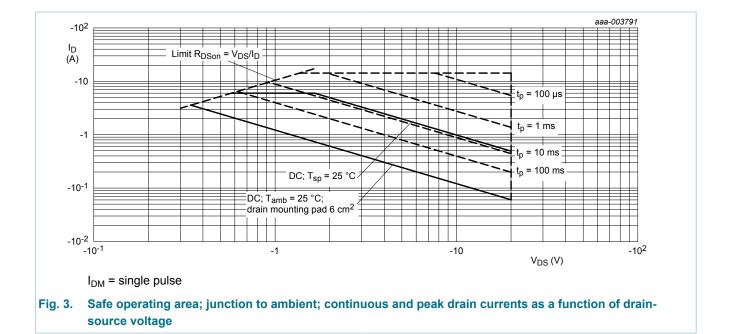


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_D(25^{\circ}C)} \times 100 \%$$

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9. Thermal characteristics

Table 6. T	hermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or	L.	l				
fror	thermal resistance	in free air	[1]	-	212	244	K/W
	from junction to ambient		[2]	-	90	104	K/W
	ampient	in free air; t ≤ 5 s	[2]	-	55	64	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	11	15	K/W

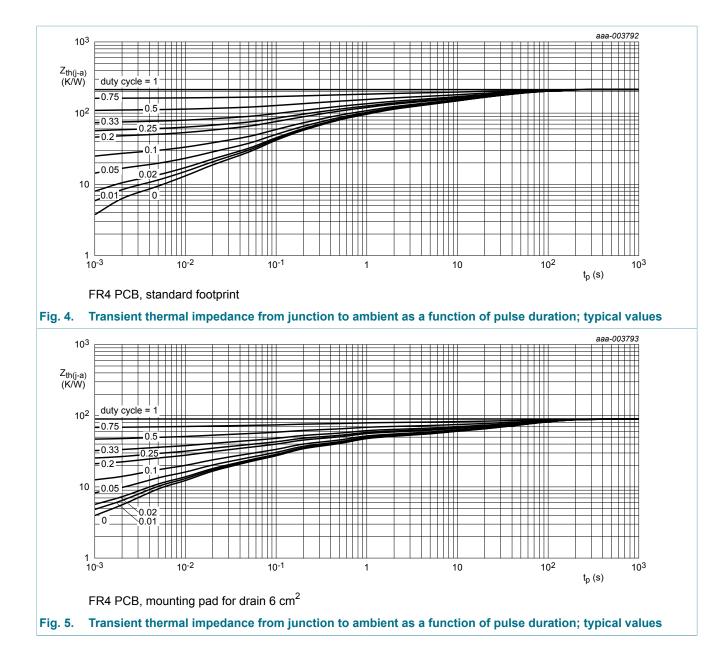
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

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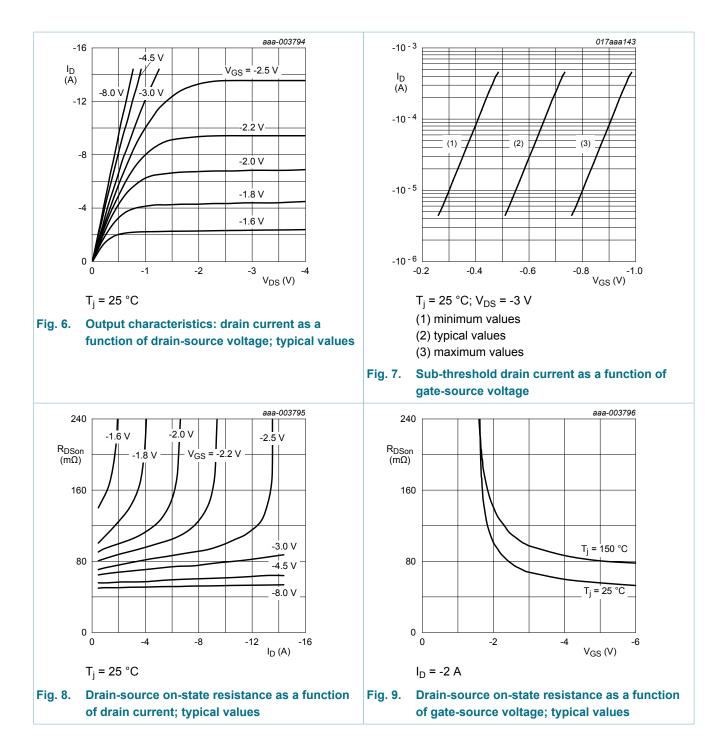
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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics (per transistor)					
V _{(BR)DSS}	drain-source breakdown voltage	I_D = -250 µA; V_{GS} = 0 V; T_j = 25 °C	-20	-	-	V
V _{GSth}	gate-source threshold voltage	$I_D = -250 \ \mu A; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^{\circ}C$	-0.45	-0.7	-0.95	V
I _{DSS}	drain leakage current	V_{DS} = -20 V; V_{GS} = 0 V; T_j = 25 °C	-	-	-1	μA
		V _{DS} = -20 V; V _{GS} = 0 V; T _j = 150 °C	-	-	-10	μA
I _{GSS}	gate leakage current	V _{GS} = 8 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V_{GS} = -8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
R _{DSon} drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -2 A; T _j = 25 °C	-	58	67	mΩ	
	V _{GS} = -4.5 V; I _D = -2 A; T _j = 150 °C	-	82	95	mΩ	
	V_{GS} = -2.5 V; I _D = -1.5 A; T _j = 25 °C	-	74	95	mΩ	
	V _{GS} = -1.8 V; I _D = -1 A; T _j = 25 °C	-	97	137	mΩ	
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -2 A; T _j = 25 °C	-	9	-	S
Dynamic ch	aracteristics (per transist	or)	I			
Q _{G(tot)}	total gate charge	V_{DS} = -10 V; I _D = -2 A; V _{GS} = -4.5 V;	-	6.3	9.5	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1.2	-	nC
Q _{GD}	gate-drain charge		-	0.9	-	nC
C _{iss}	input capacitance	V_{DS} = -10 V; f = 1 MHz; V_{GS} = 0 V;	-	804	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	95	-	pF
C _{rss}	reverse transfer capacitance		-	66	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -10 V; I _D = -2 A; V _{GS} = -4.5 V;	-	7	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	15	-	ns
t _{d(off)}	turn-off delay time		-	41	-	ns
t _f	fall time	1	-	14	-	ns
Source-drai	n diode (per transistor)	· · · · · · · · · · · · · · · · · · ·	1	_		
V _{SD}	source-drain voltage	I _S = -0.5 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.7	-1.2	V

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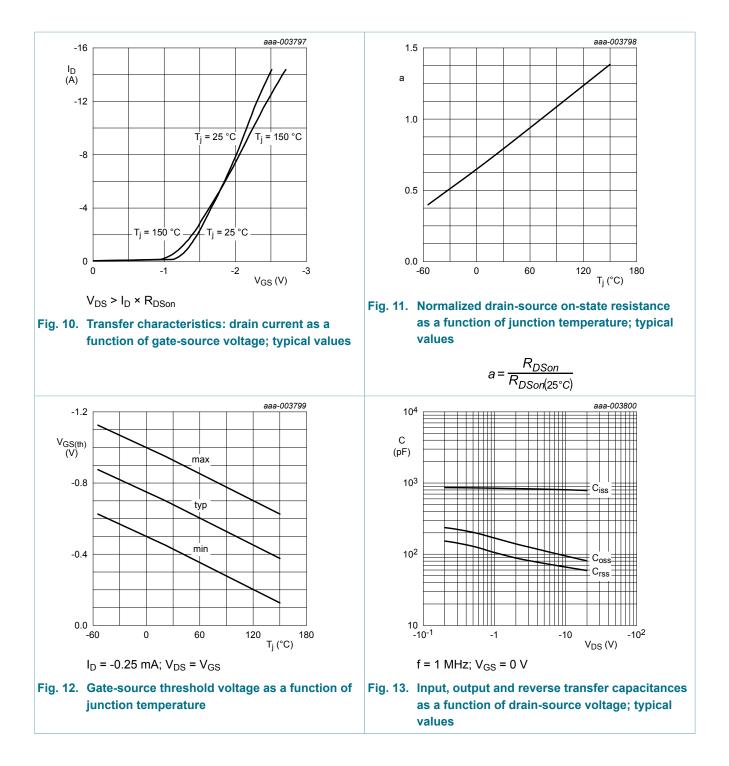
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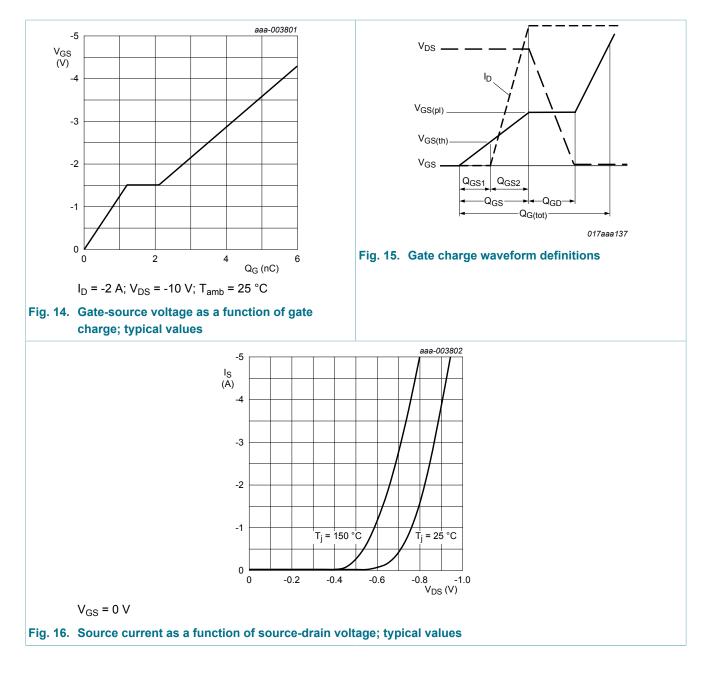
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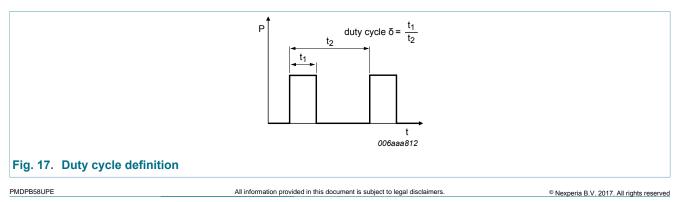


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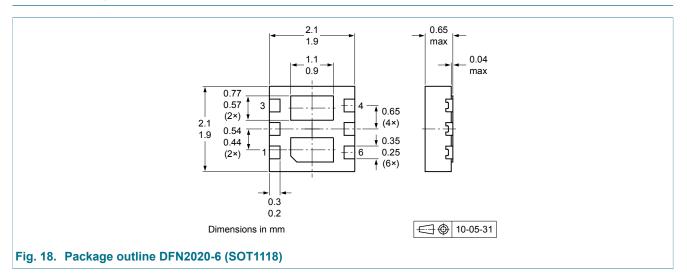


11. Test information

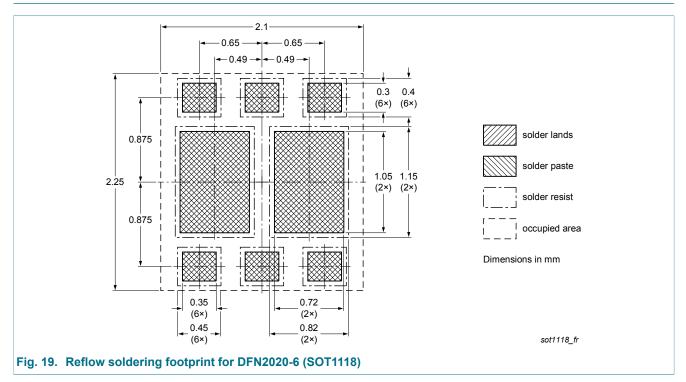


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12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMDPB58UPE v.2	20160203	Product data sheet	-	PMDPB58UPE v.1		
Modifications:	Figure 9: corrected					
PMDPB58UPE v.1	20120619	Product data sheet	-	-		

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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